

L Number	Hits	Search Text	DB	Time stamp
1	6751	cache near (miss or missing or misses or mis-hit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:14
2	370	(predict\$3 or speculat\$3) with (cache near (miss or missing or misses or mis-hit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:36
4	28	correlat\$ with (cache near (miss or missing or misses or mis-hit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:16
5	45	successor with key	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:17
3	16	711/128.ccls. and ((predict\$3 or speculat\$3) with (cache near (miss or missing or misses or mis-hit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:19
6	11121	prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3 or read-ahead or (read adj advance)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:19
7	519	(prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3 or read-ahead or (read adj advance)) with (cache near (miss or missing or misses or mis-hit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:19
8	23	711/128.ccls. and ((prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3 or read-ahead or (read adj advance)) with (cache near (miss or missing or misses or mis-hit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:19
9	21	(711/128.ccls. and ((prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3 or read-ahead or (read adj advance)) with (cache near (miss or missing or misses or mis-hit)))) not (711/128.ccls. and ((predict\$3 or speculat\$3) with (cache near (miss or missing or misses or mis-hit))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:24
10	3	correlat\$ same (successor with key)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:24
11	557	address\$3 adj (stride or stream)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:26
12	5	correlat\$ same (address\$3 adj (stride or stream))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:26
13	6	(address\$3 adj (stride or stream)) and 711/128.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:30

14	32	(predict\$3 or speculat\$3) same (address\$3 adj (stride or stream))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:29
15	1878	(predict\$3 or speculat\$3) near (table or database or graph or map or mapping)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:30
16	12	((predict\$3 or speculat\$3) near (table or database or graph or map or mapping)) and 711/128.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:32
17	5769	(current or present) adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:32
18	2715	(previous or prior or preceding) adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:33
19	12303	(succeeding or following or next) adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:34
20	167	((current or present) adj address) with ((previous or prior or preceding) adj address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:34
21	333	((current or present) adj address) with ((succeeding or following or next) adj address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:34
23	0	correlat\$ with (((current or present) adj address) with ((succeeding or following or next) adj address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:34
22	3	correlat\$ with (((current or present) adj address) with ((previous or prior or preceding) adj address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:35
24	8	(map\$4 or link\$3) with (((current or present) adj address) with ((previous or prior or preceding) adj address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:35
25	7	(map\$4 or link\$3) with (((current or present) adj address) with ((succeeding or following or next) adj address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:36
26	4	(predict\$3 or speculat\$3) with ((succeeding or following or next) adj address) with ((current or present) adj address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/10/29 15:36

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(address <near> correlation) and cache

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((address <near> correlation) and cache) and ((1950 <in> py) or (1951 <in> py) or (19
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4 Experimentally characterizing the behavior of multiprocessor

memory systems: a case study

Gallivan, K.; Gannon, D.; Jalby, W.; Malony, A.; Wijshoff, H.;
Software Engineering, IEEE Transactions on , Volume: 16 Issue: 2 ,
Feb. 1990
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5 Offline program re-mapping to improve branch prediction efficiency in embedded systems

Brown, S.S.; Asher, J.; Mangione-Smith, W.H.;
Design Automation Conference, 2000. Proceedings of the ASP-DAC
2000. Asia and South Pacific , 25-28 Jan. 2000
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6 Compression-based program characterization for improving cache memory performance

Phalke, V.; Gopinath, B.;
Computers, IEEE Transactions on , Volume: 46 Issue: 11 , Nov. 1997
Page(s): 1174 -1186

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7 Experimental analysis of coherency behavior of shared memory scientific applications

Acquaviva, J.T.; Jalby, W.;
Modeling, Analysis and Simulation of Computer and
Telecommunication Systems, 2000. Proceedings. 8th International
Symposium on , 29 Aug.-1 Sept. 2000
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8 A vector architecture for higher-order moments estimation

Alves, J.C.; Puga, A.; Corte-Real, L.; Matos, J.S.;
Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997
IEEE International Conference on , Volume: 5 , 21-24 April 1997
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9 Parallel trace-driven cache simulation by time partitioning

Heidelberger, P.; Stone, H.S.;

Simulation Conference, 1990. Proceedings., Winter , 9-12 Dec. 1990

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10 Next cache line and set prediction

Calder, B.; Grunwald, D.;

Computer Architecture, 1995. Proceedings. 22nd Annual International Symposium on , 22-24 June 1995

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11 Evaluating the performance of active cache management schemes

Tam, E.S.; Rivers, J.A.; Srinivasan, V.; Tyson, G.S.; Davidson, E.S.;

Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings., International Conference on , 5-7 Oct. 1998

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